

FIG 1A

NMOS transistor

NPN bipolar transistor

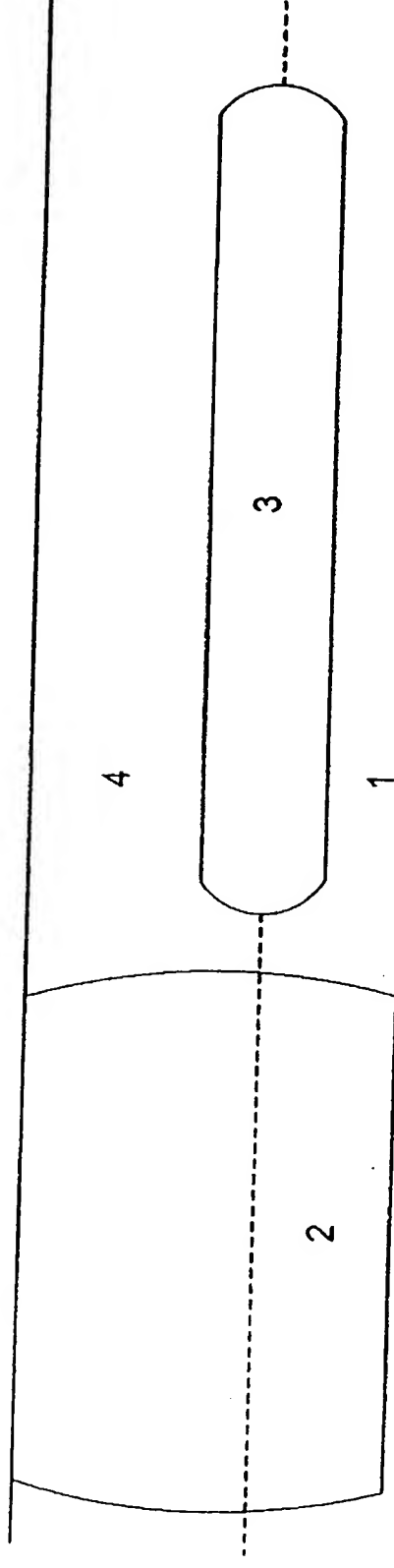


FIG 1B

vertical PNP bipolar transistor

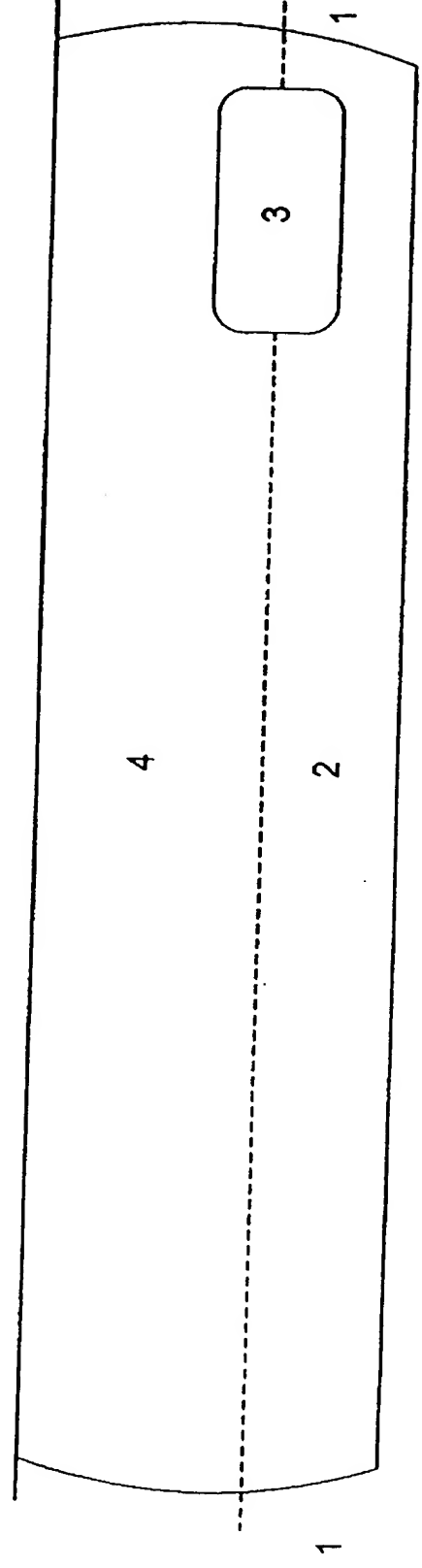
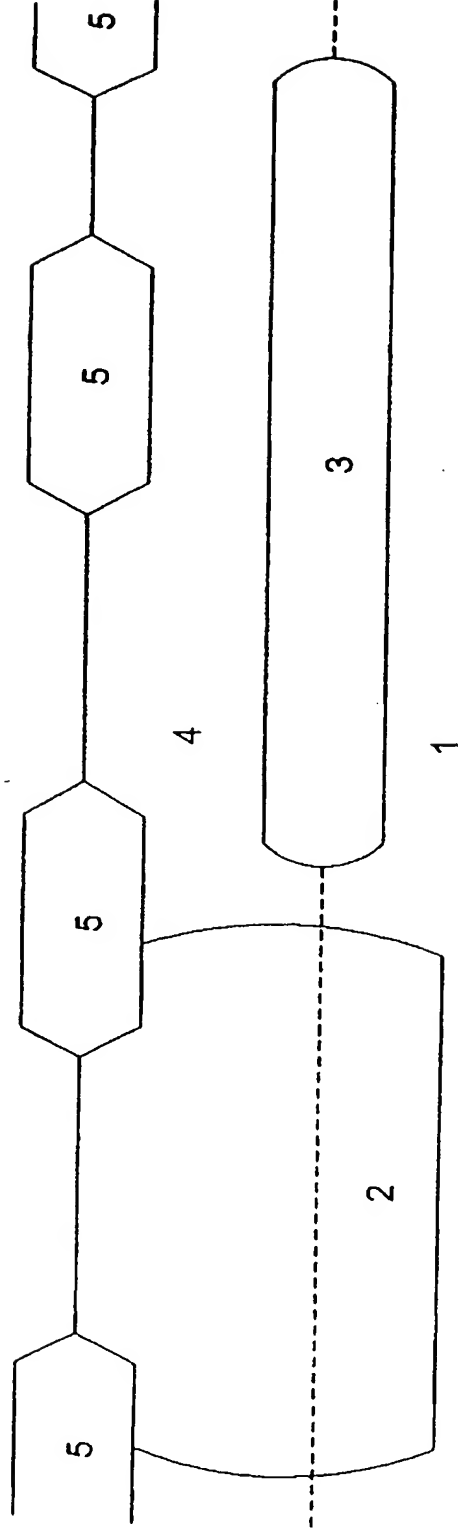


FIG 2A

NMOS transistor



NPN bipolar transistor

FIG 2B

vertical PNP bipolar transistor

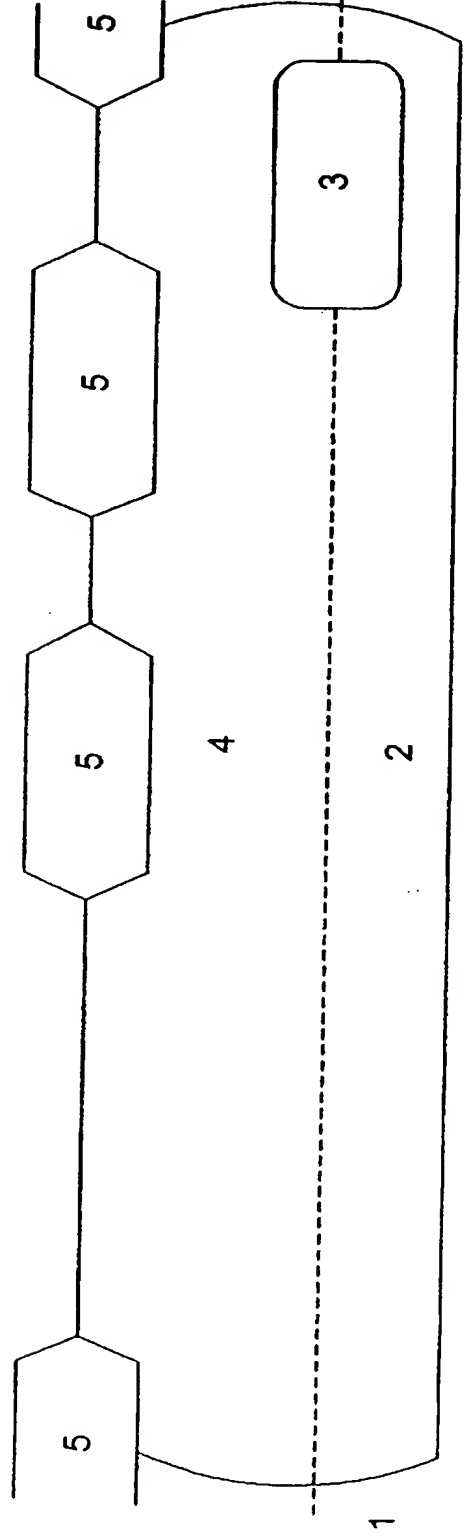
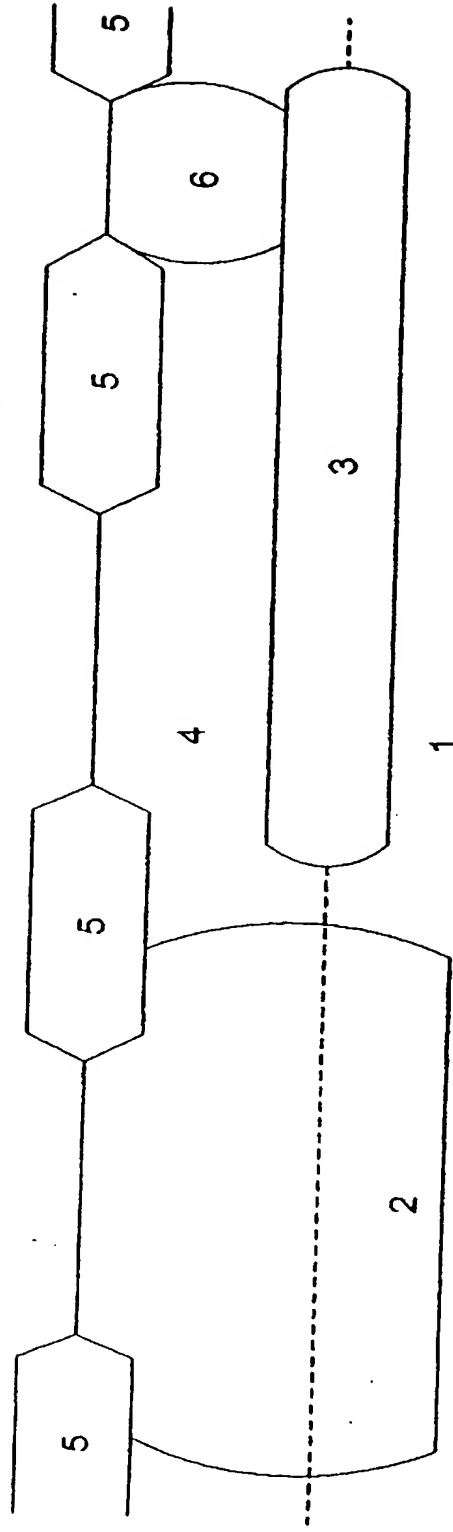


FIG 3A

NMOS transistor



NPN bipolar transistor

FIG 3B

vertical PNP bipolar transistor

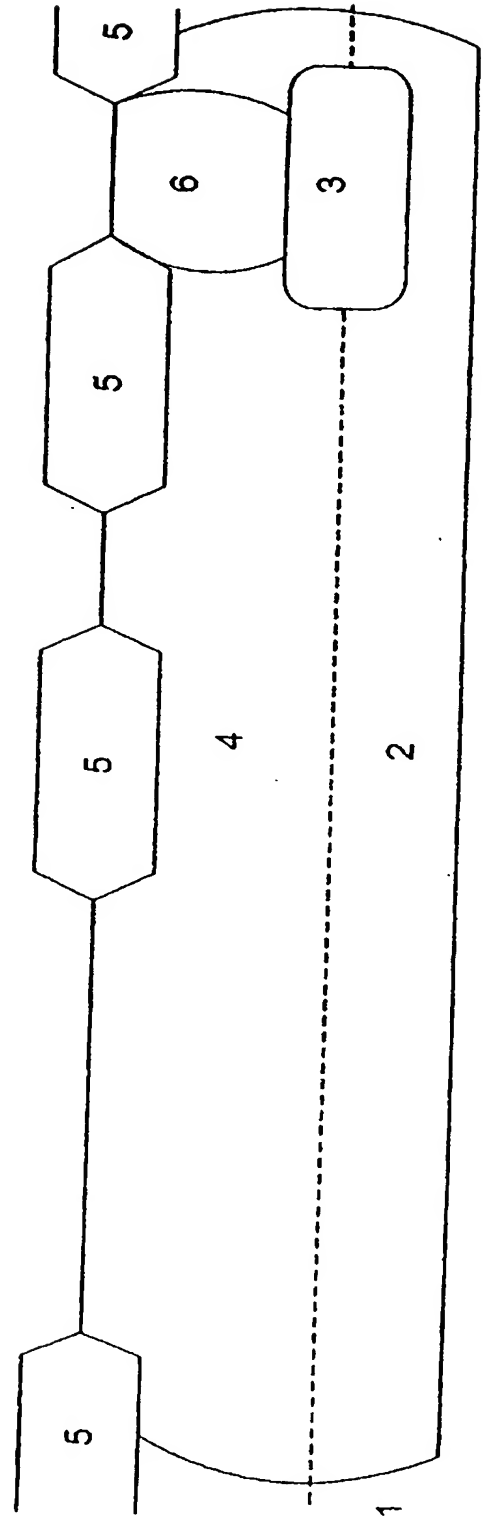


FIG 3C

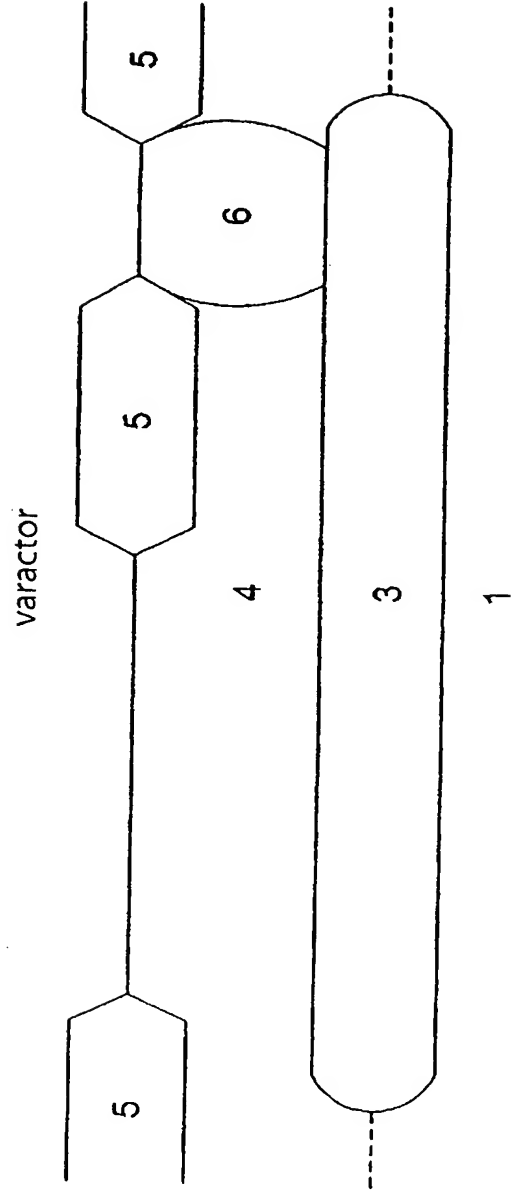


FIG 3D

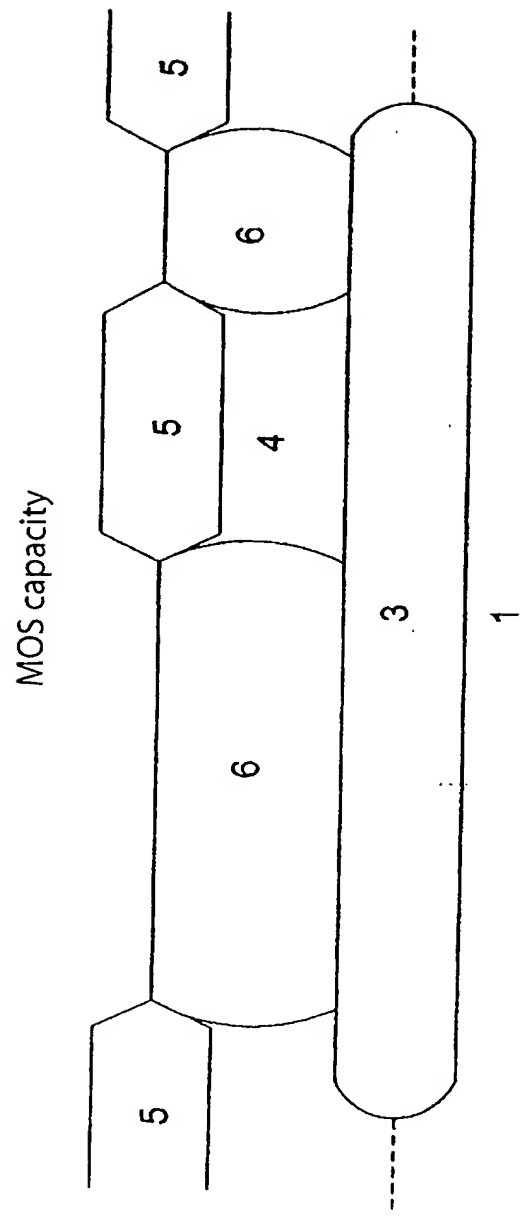


FIG 4

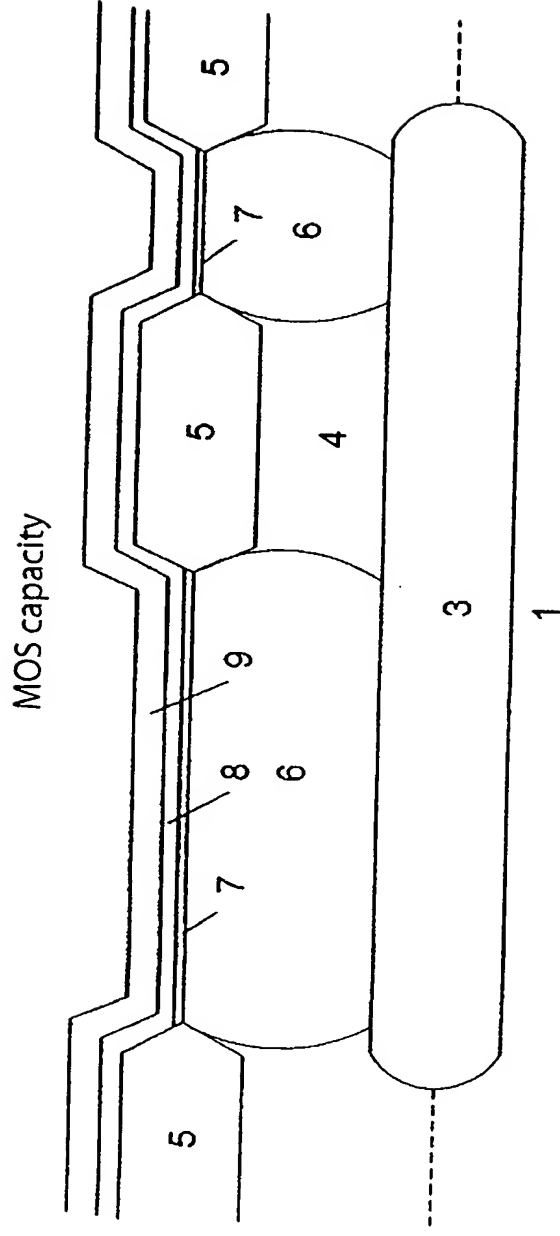


FIG 5

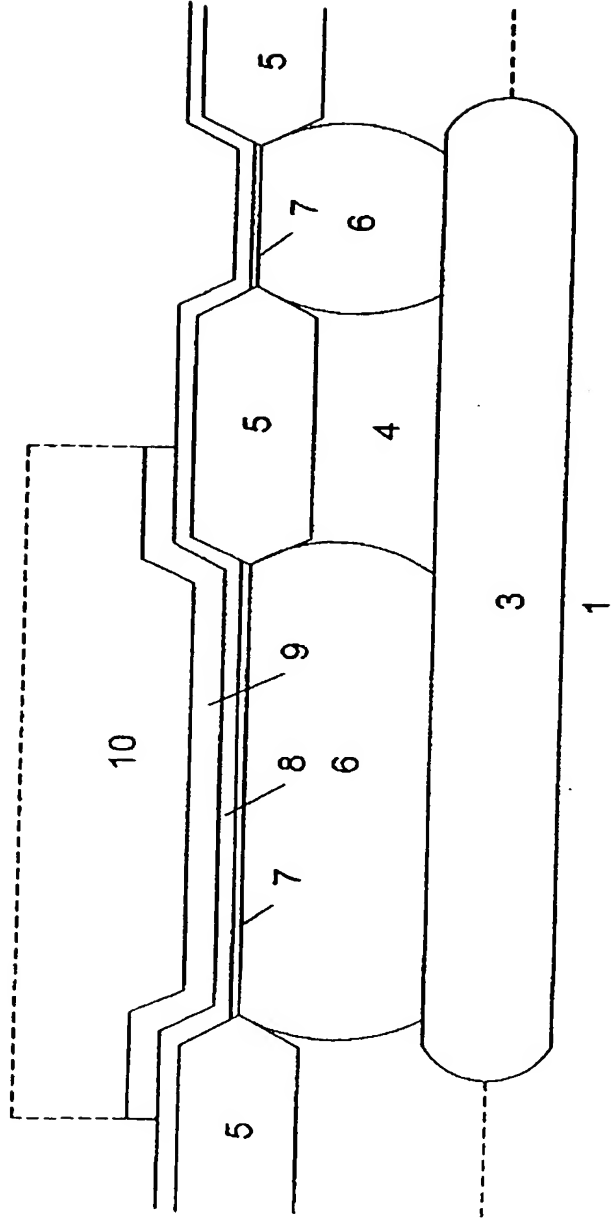


FIG 6

MOS capacity

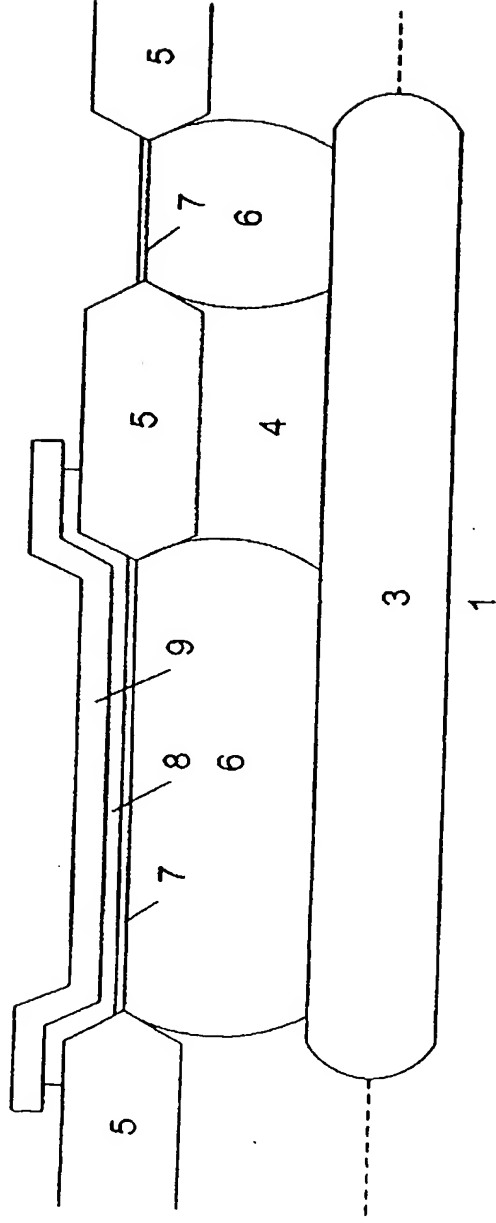


FIG 7A

vertical PNP bipolar transistor

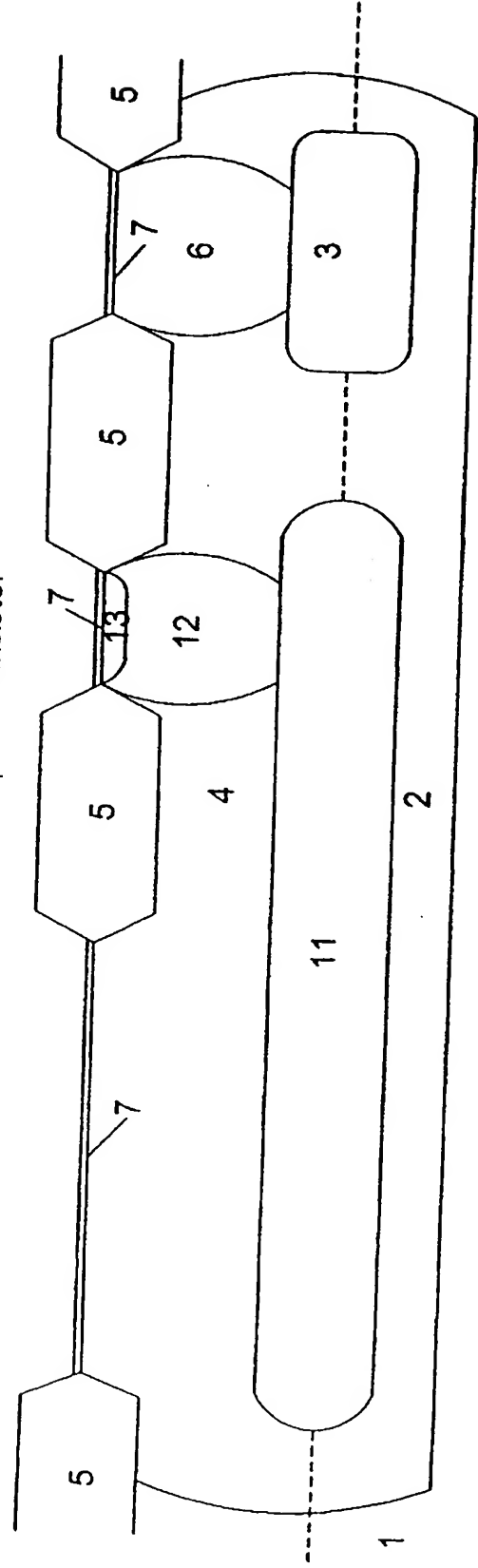


FIG 7B

NMOS transistor

NPN bipolar transistor

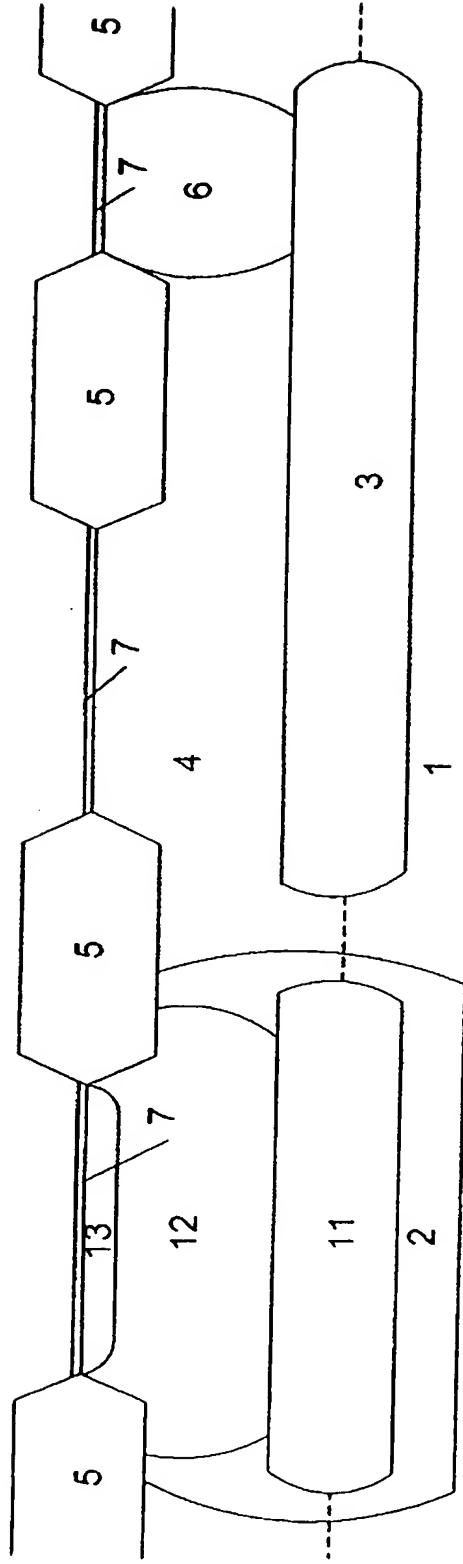
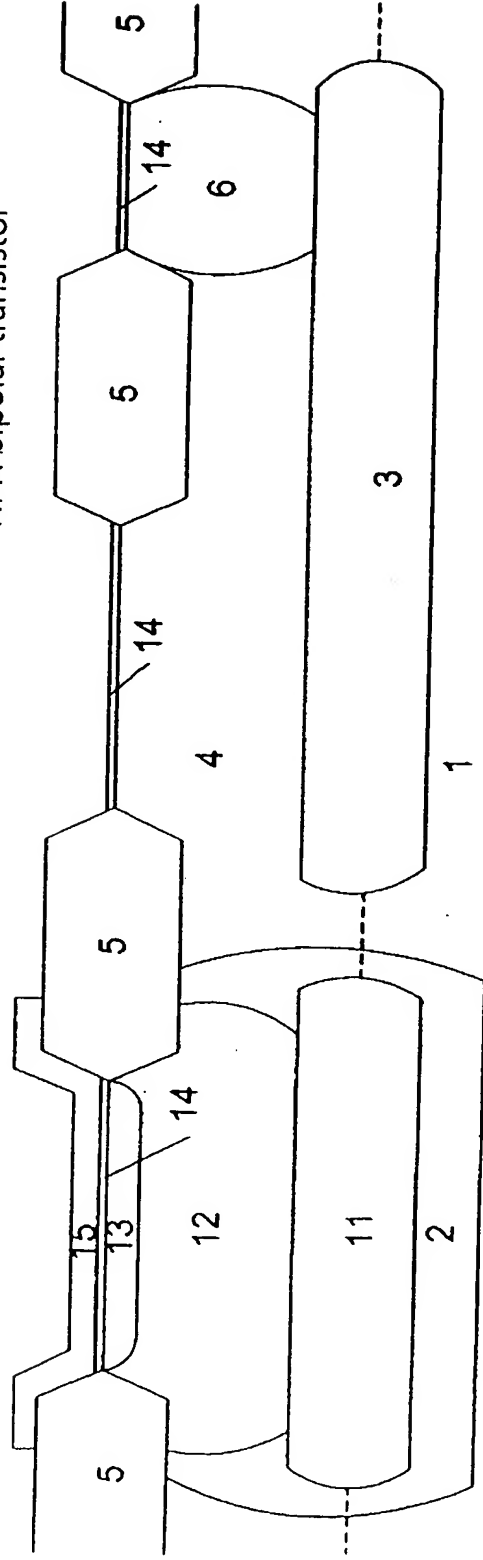


FIG 8

NMOS transistor

NPN bipolar transistor





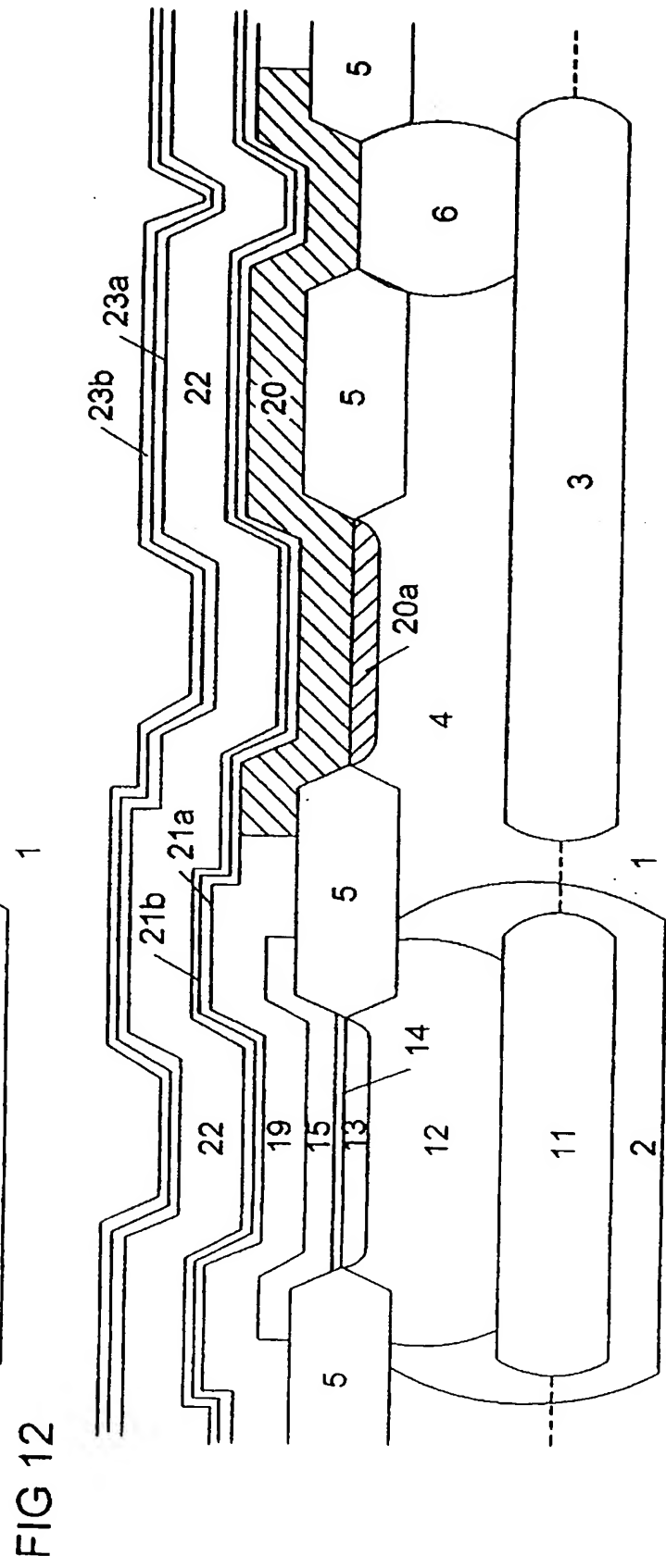
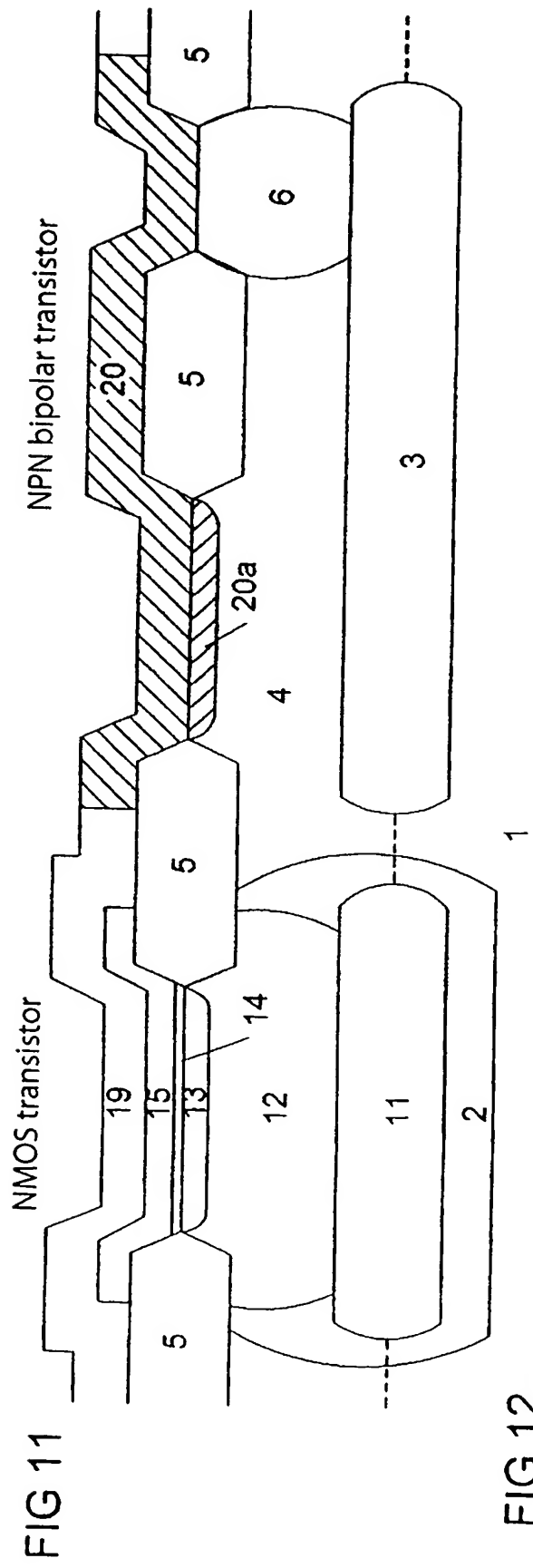
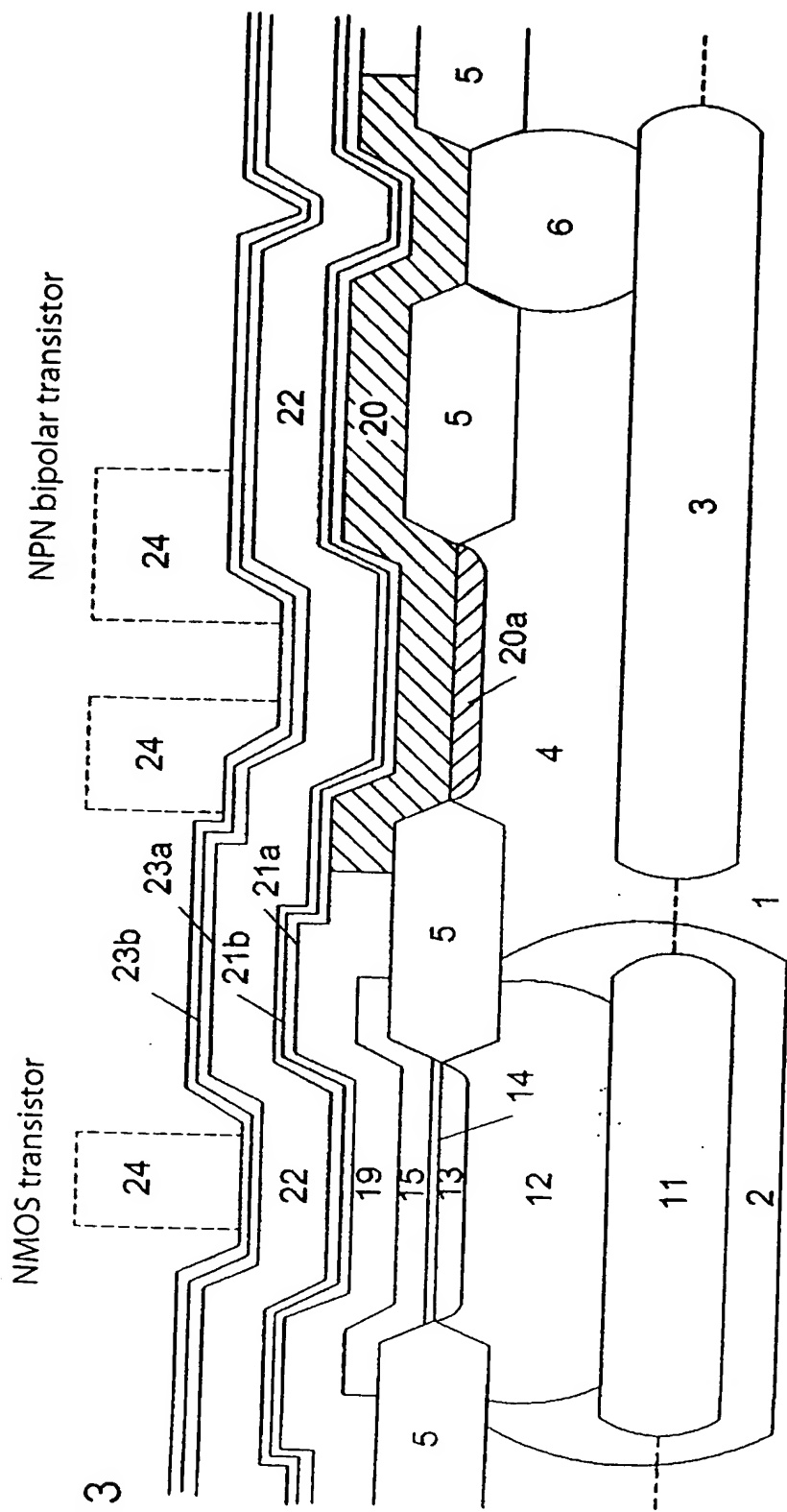


FIG 13



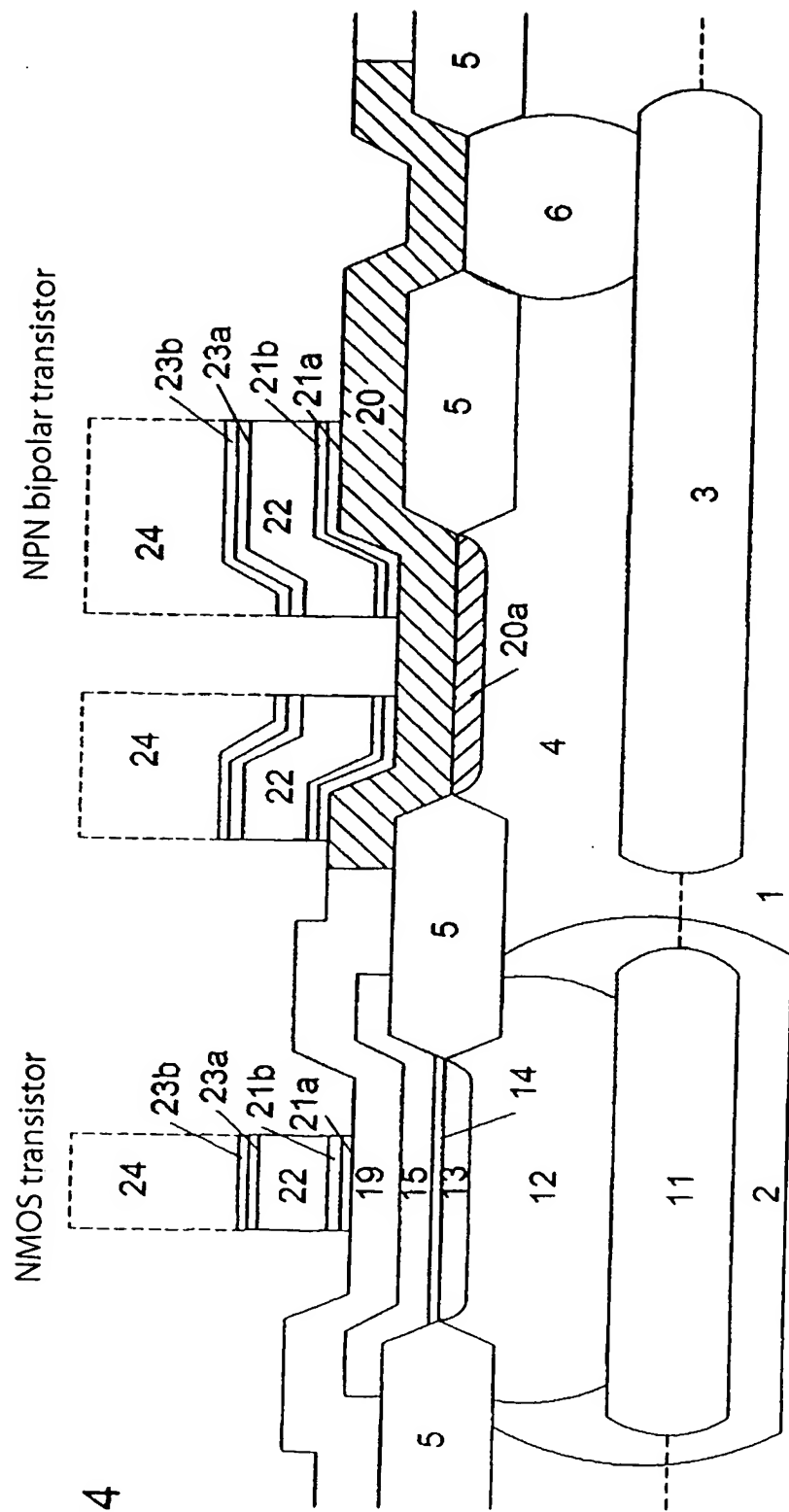
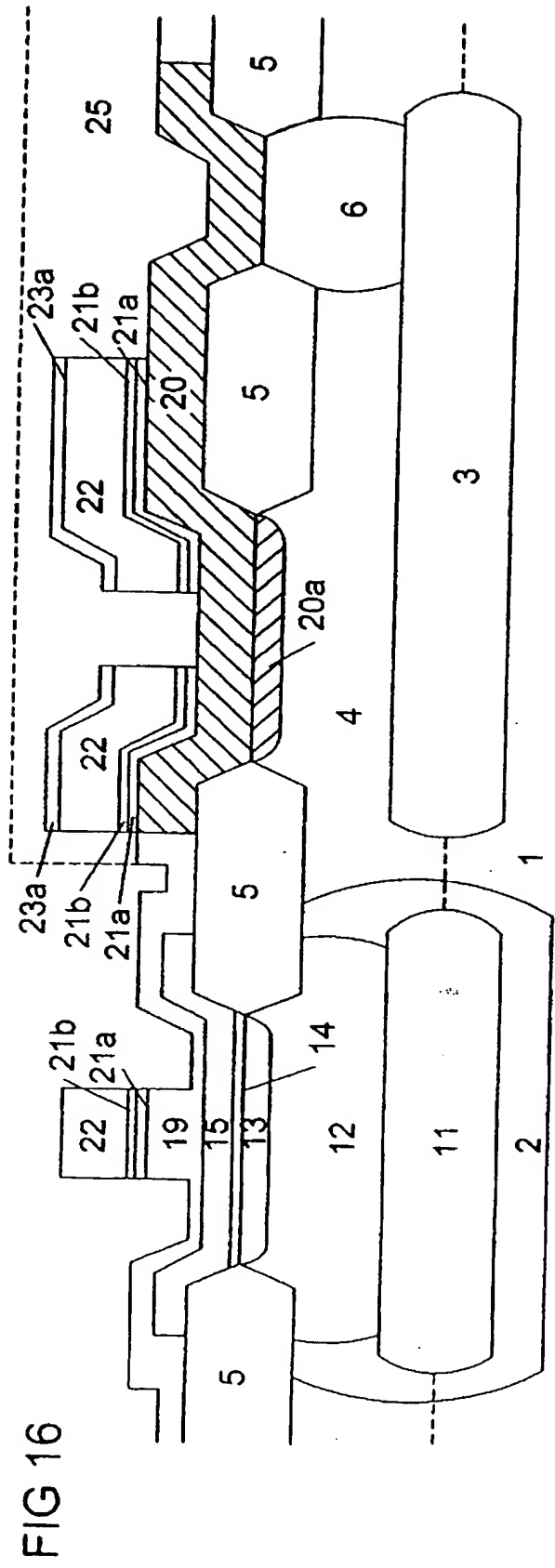
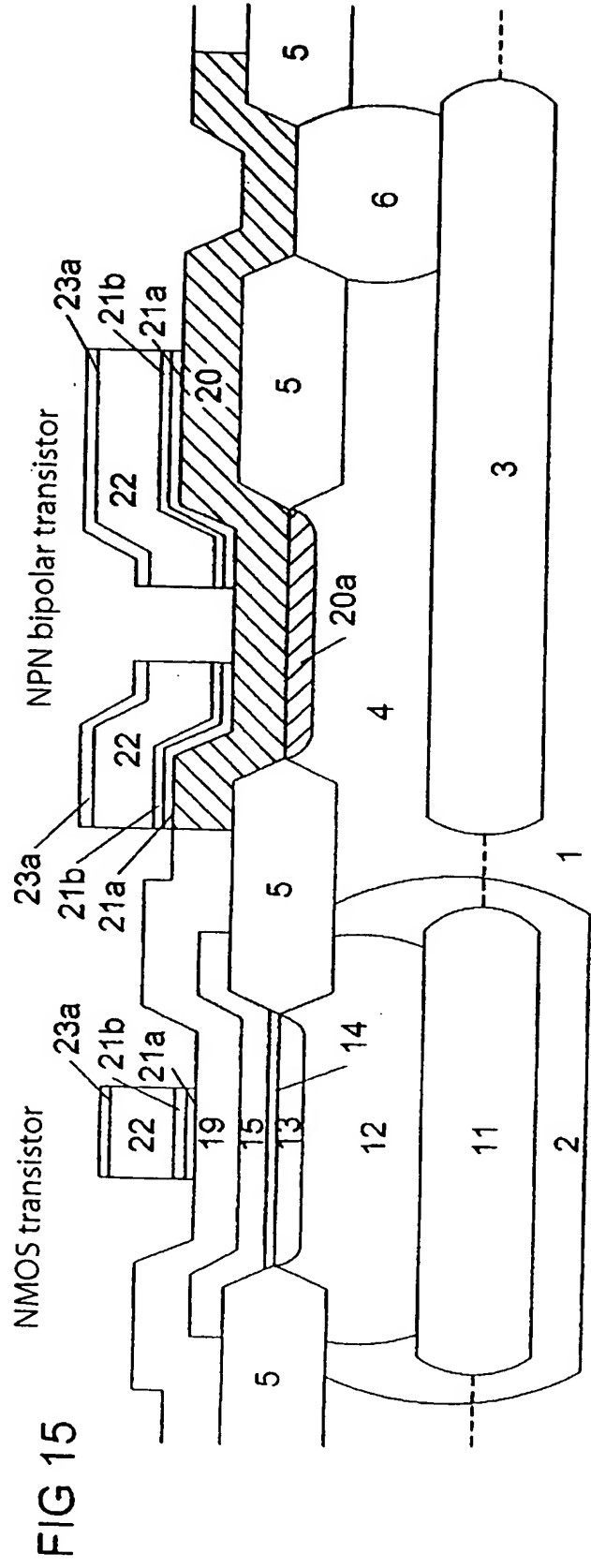
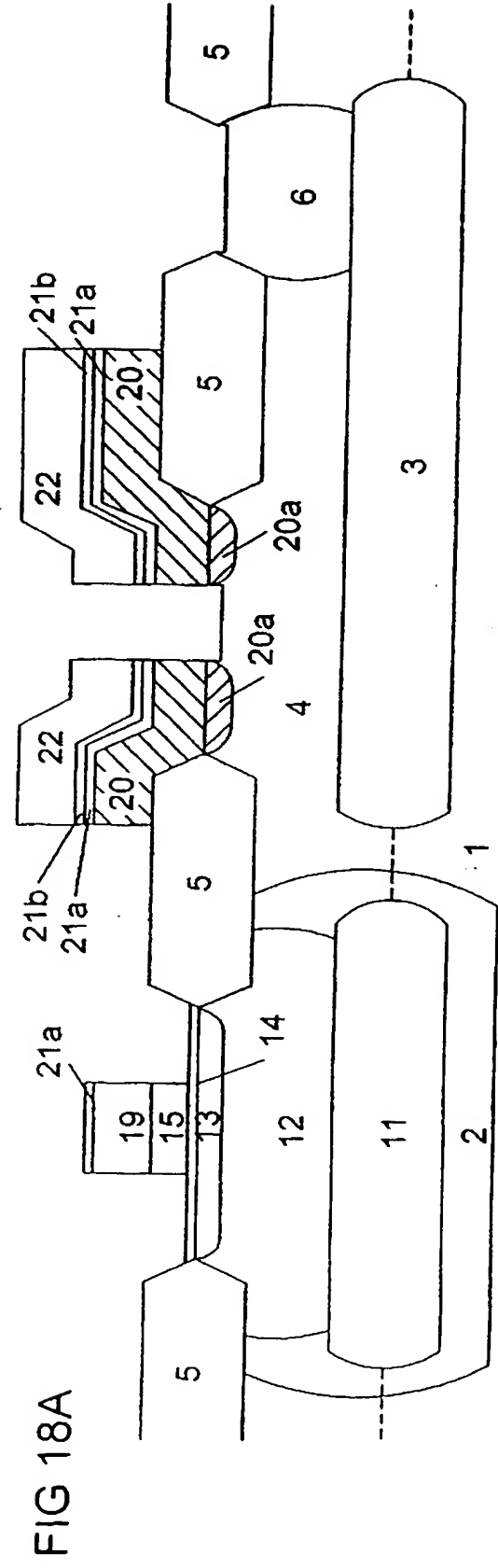
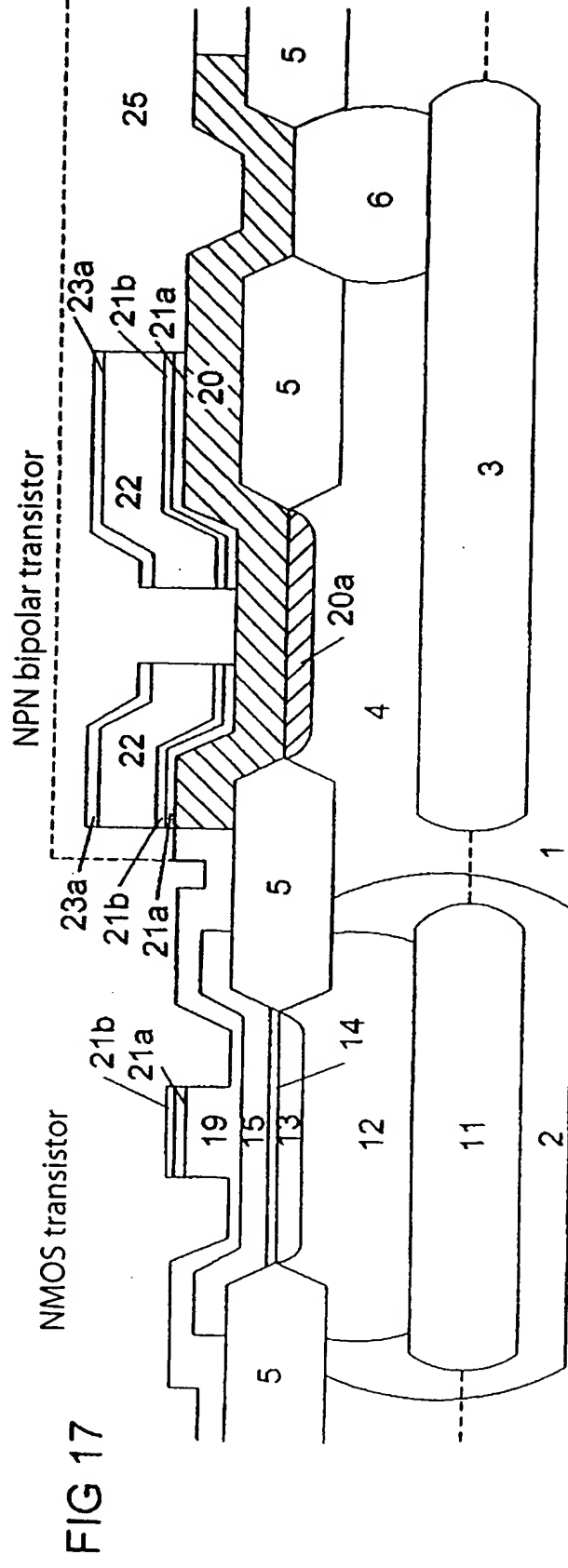


FIG 14





vertical PNP bipolar transistor

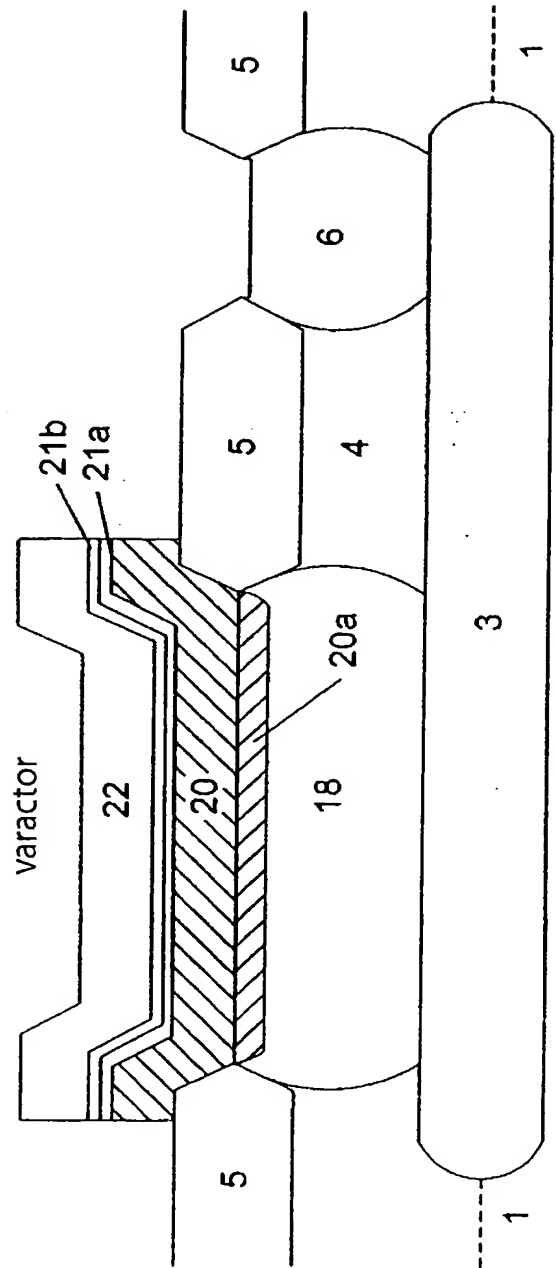
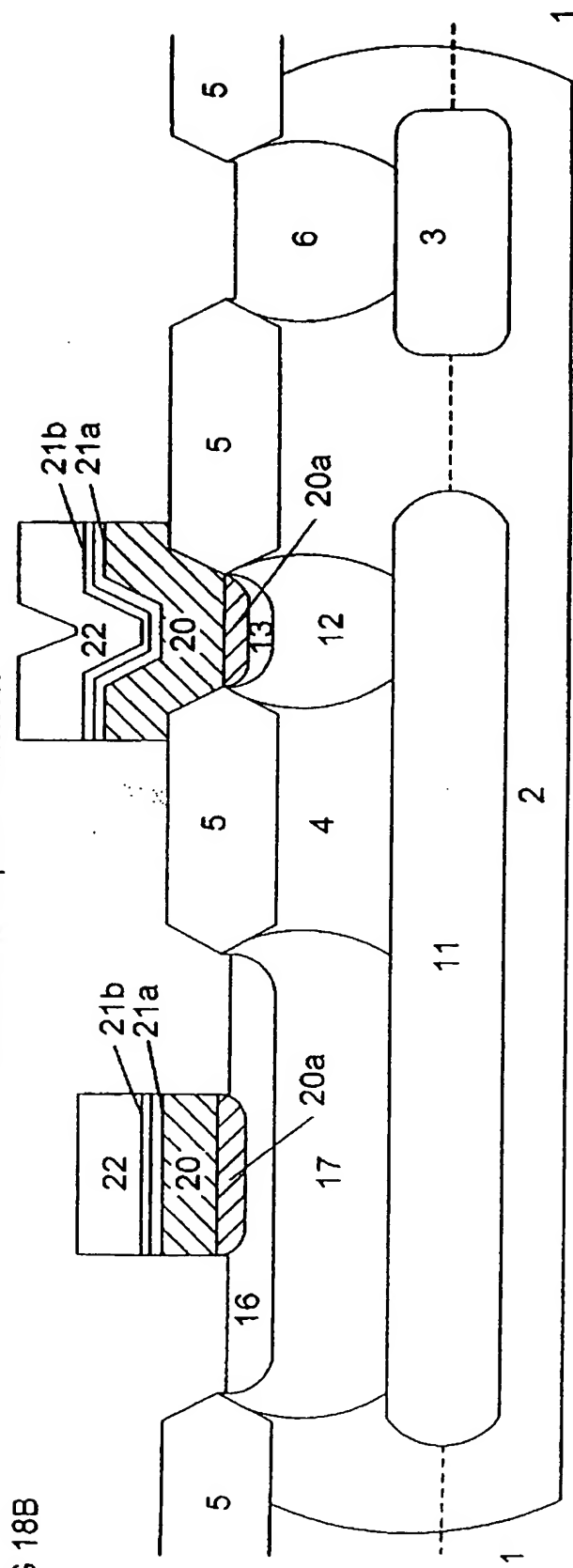
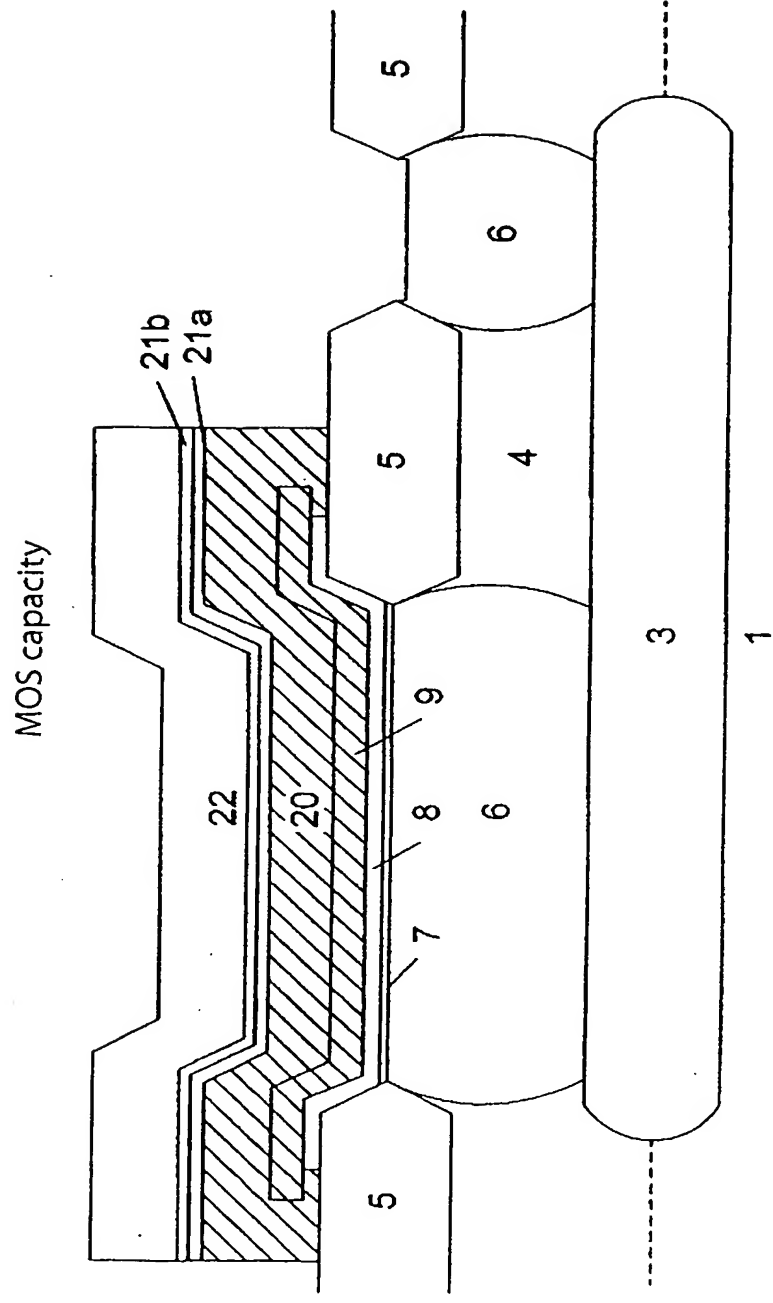
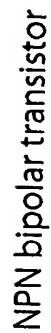


FIG 18C

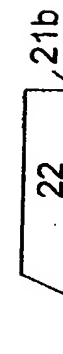
FIG 18D



## NMOS transistor



213426



## NMOS transistor

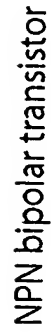


FIG 23

NMOS transistor

NPN bipolar transistor

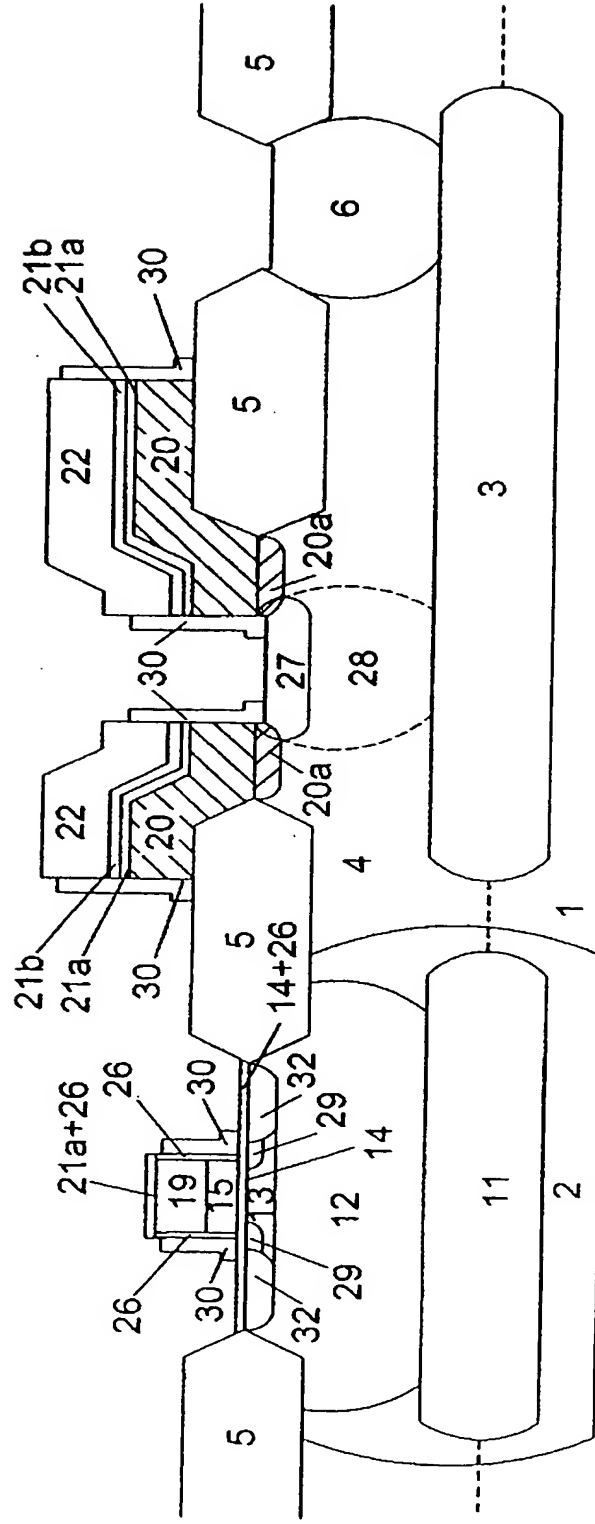


FIG 24A

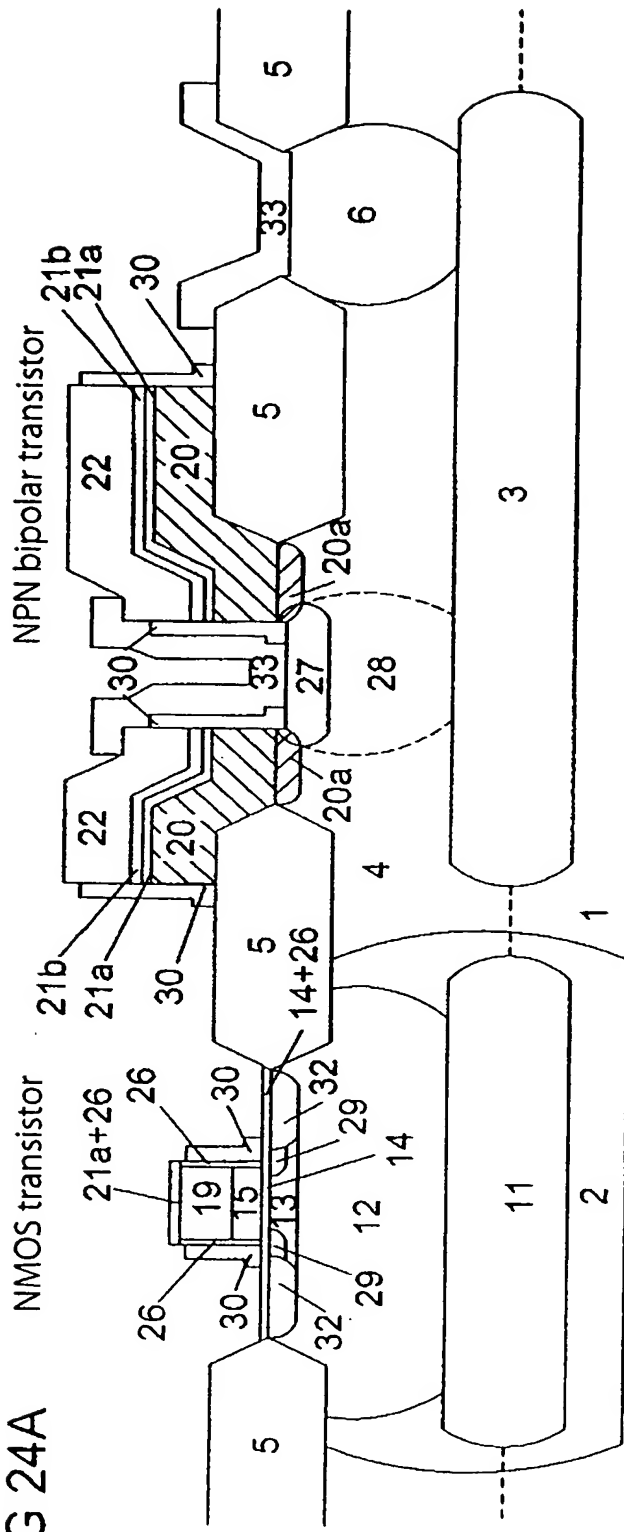


FIG 24B

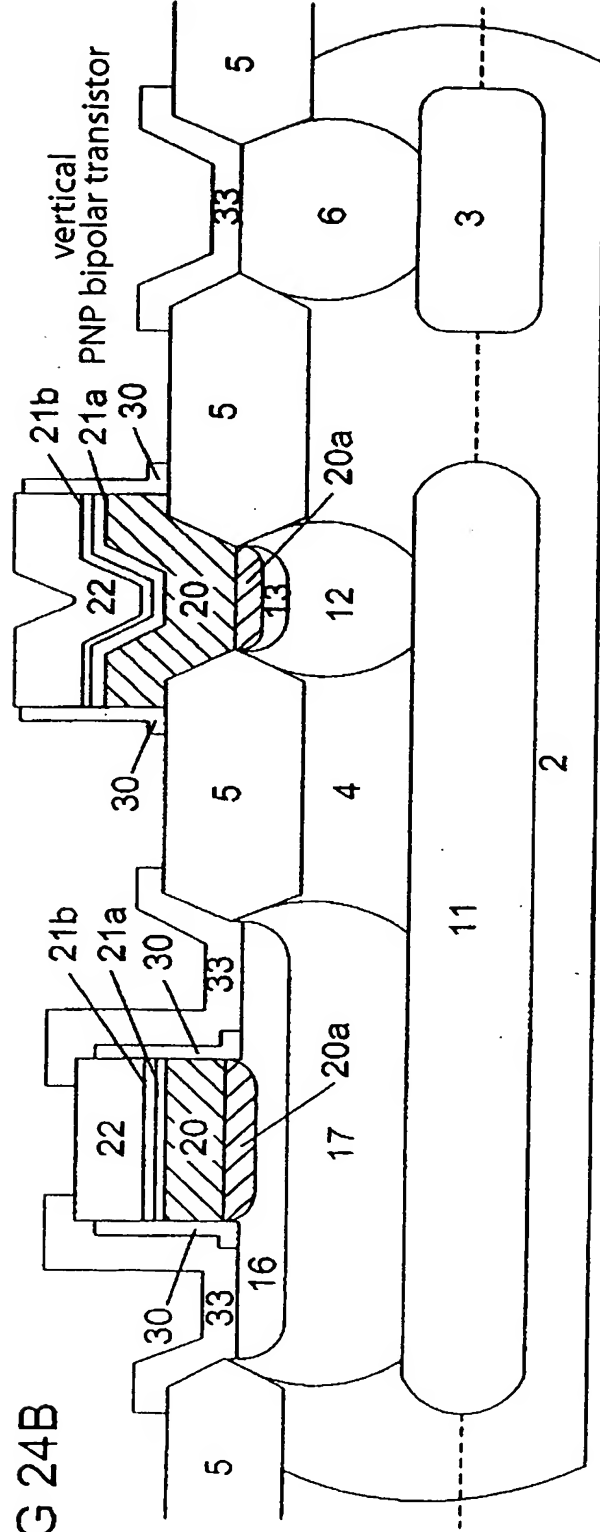


FIG 24C

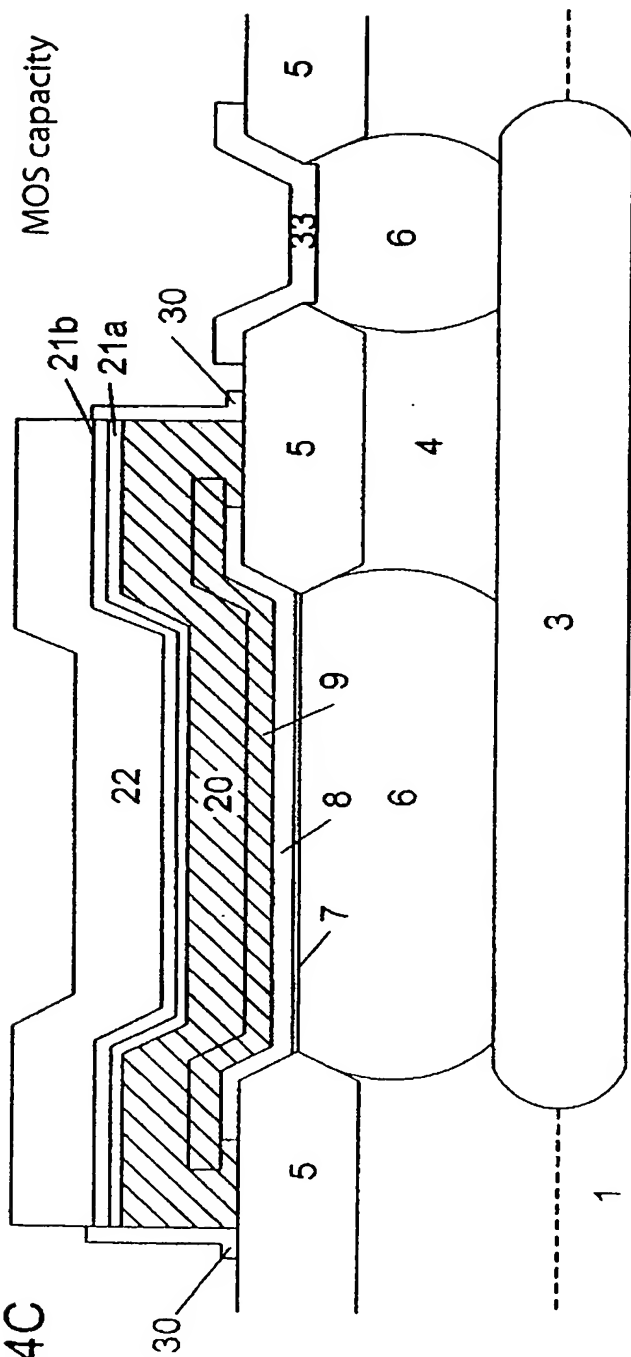


FIG 24D

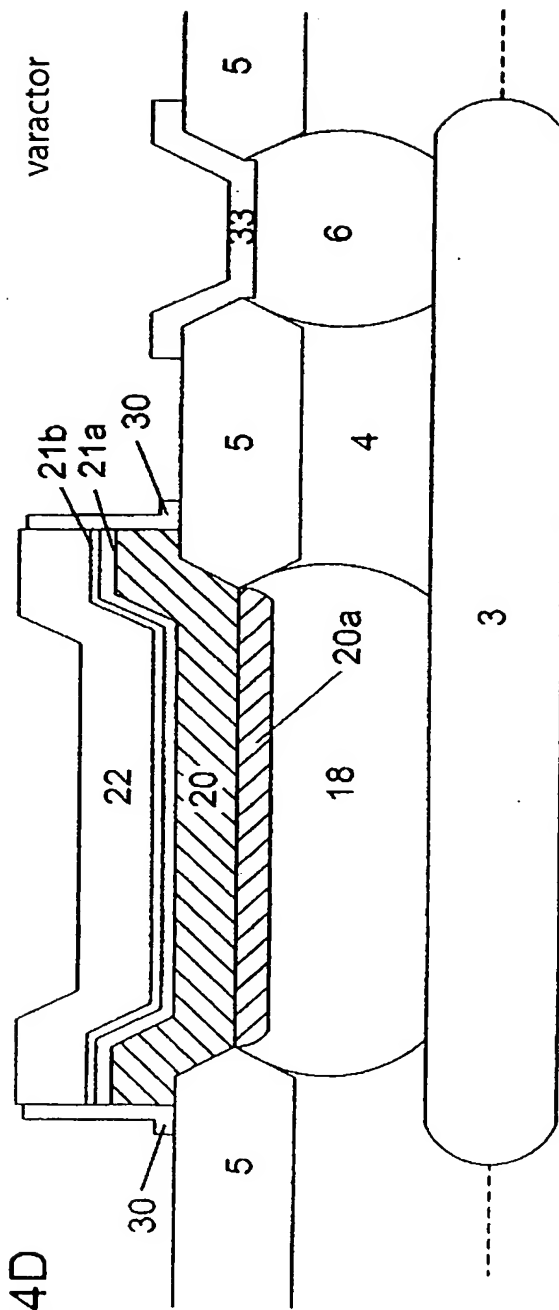


FIG 25

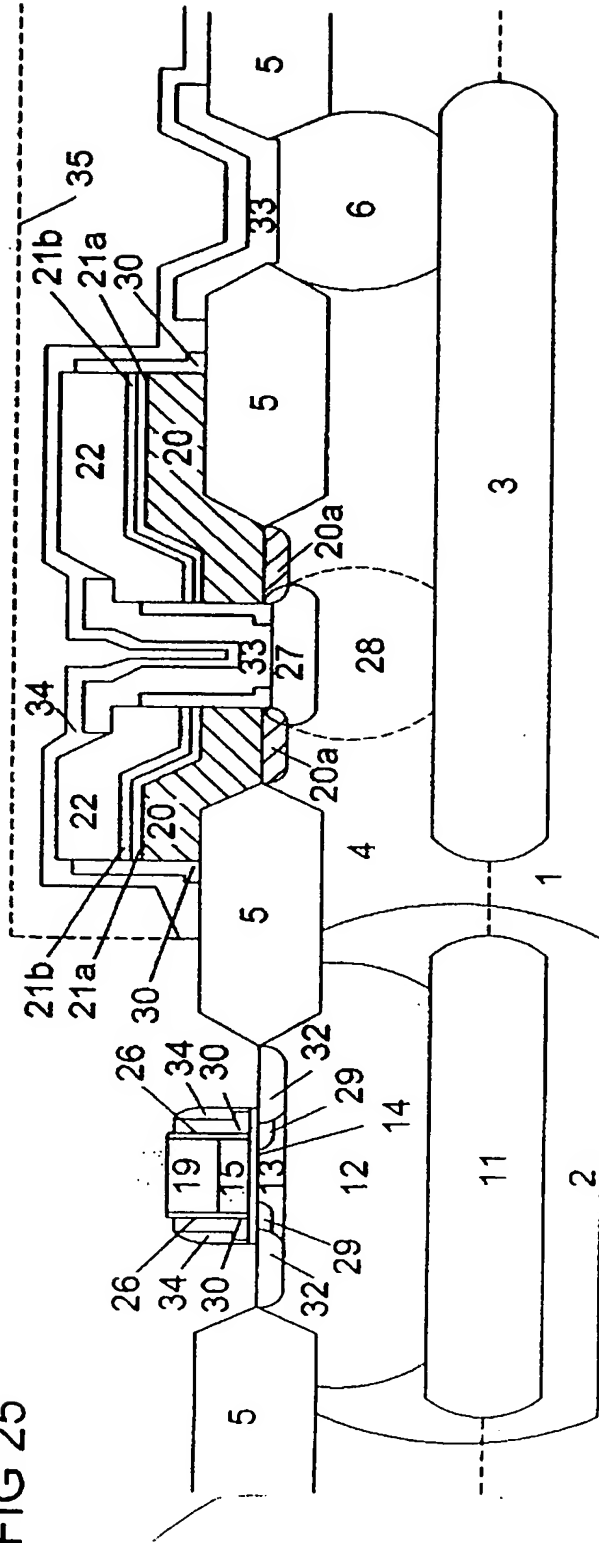


FIG 26

